Product/Process Change Notice - PCN 19_0263 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title: LT8650S Component, Substrate Layout and Datasheet Specification Change

Publication Date: 04-May-2020

Effectivity Date: 06-Aug-2020 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:
Initial Release

Description Of Change:

Please be advised that Analog Devices has made minor change to the LT8650S due to End of Life of capacitors by the supplier. Change to new capacitors resulted in minor substrate layout change to accommodate new capacitors.
Capacitors:
1) Changed from 0.22uF, X7S, 6.3V, 10% to 0.1uF, X8L, 25V, 10%
2) Change from 0.1uF, X7R, 50V, 10% to 10nF, X8R, 50V, 10%
Substrate:
1) Layout change for new capacitors
In addition, electrical specifications of the datasheet were changed as shown in attached red mark-up datasheet.

Reason For Change:

To improve manufacturability, quality, and reliability.
The change was initiated as part of ADI's Continuous Quality Improvement efforts.

Impact of the change (positive or negative) on fit, form, function & reliability:

The change does not affect existing applications for this device.

Product Identification *(this section will describe how to identify the changed material)*

The parts that will be assembled with the new capacitors and substrate will be identified by the date code.

Summary of Supporting Information:

Qualification has been performed per AEC-Q100, stress test qualification for integrated circuits, see attached qualification results summary.

Comments

The capacitor and substrate changes were qualified by performing characterization over the full operating junction temperature range and through rigorous engineering evaluation. In addition, the product successfully completed HTOL, ESD, Latch-Up stress testing.

Supporting Documents

Attachment 1: Type: Delta Qualification Matrix

ADI_PCN_19_0263_Rev_-_PCN-Delta-Qualification-Matrix-ZVEI-4_1_LT8650S.xlsm

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Attachment 2: Type: Datasheet Specification Comparison

ADI_PCN_19_0263_Rev_-_LT8650S Datasheet changes.pdf

Attachment 3: Type: Qualification Results Summary

ADI_PCN_19_0263_Rev_-_ADI_PCN_LT8650.pdf

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.			
Americas: PCN_Americas@analog.com	**Europe:** PCN_Europe@analog.com	**Japan:** PCN_Japan@analog.com	**Rest of Asia:** PCN_ROA@analog.com

Appendix A - Affected ADI Models				
Added Parts On This Revision - Product Family / Model Number (6)				
LT8650S / LT8650SEV#PBF	LT8650S / LT8650SEV#WPBF	LT8650S / LT8650SHV#WPBF	LT8650S / LT8650SIV#PBF	LT8650S / LT8650SIV#WPBF
LT8650S / LT8650SJV#WPBF				

Appendix B - Revision History			
Rev	**Publish Date**	**Effectivity Date**	**Rev Description**
Rev. -	04-May-2020	06-Aug-2020	Initial Release